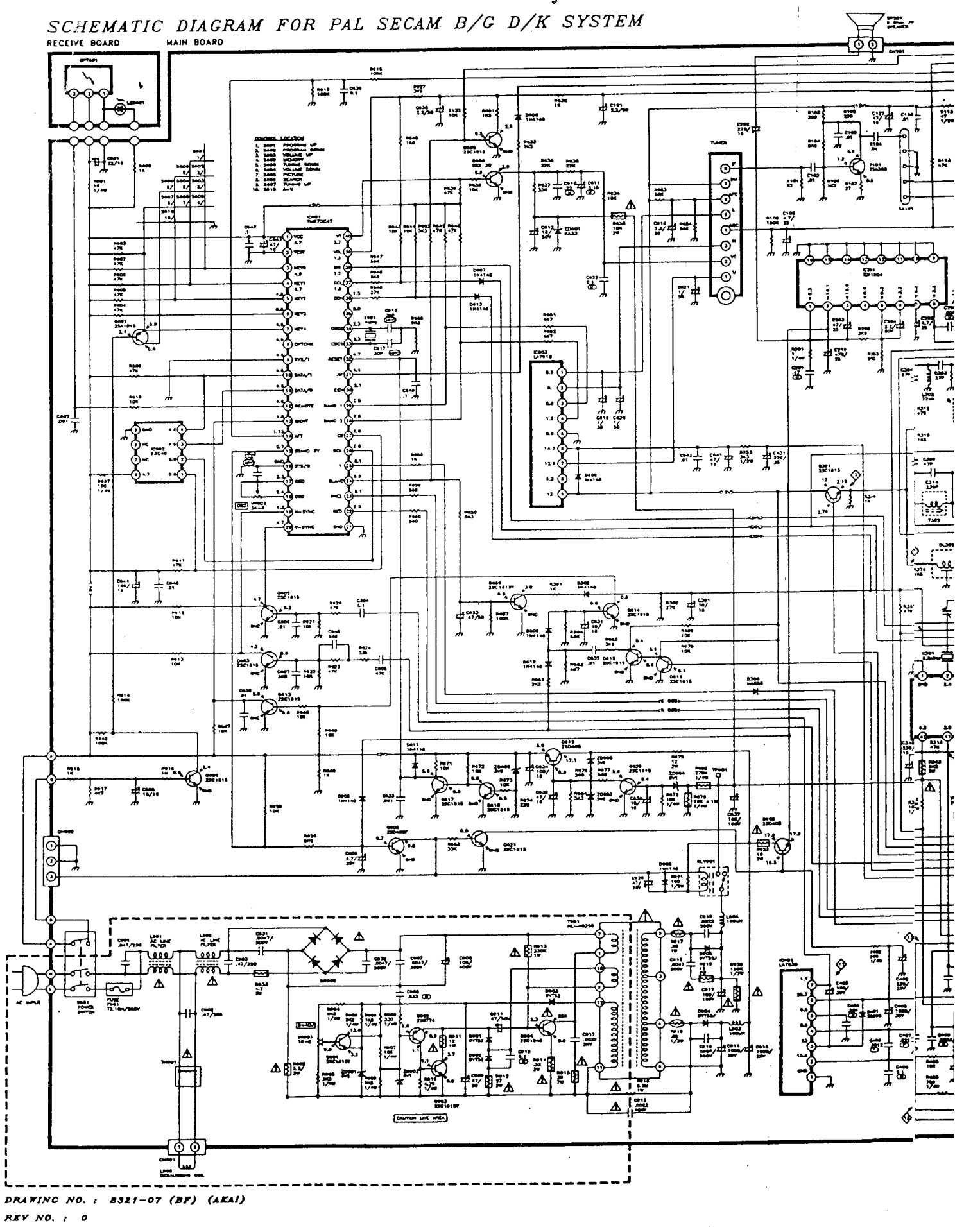
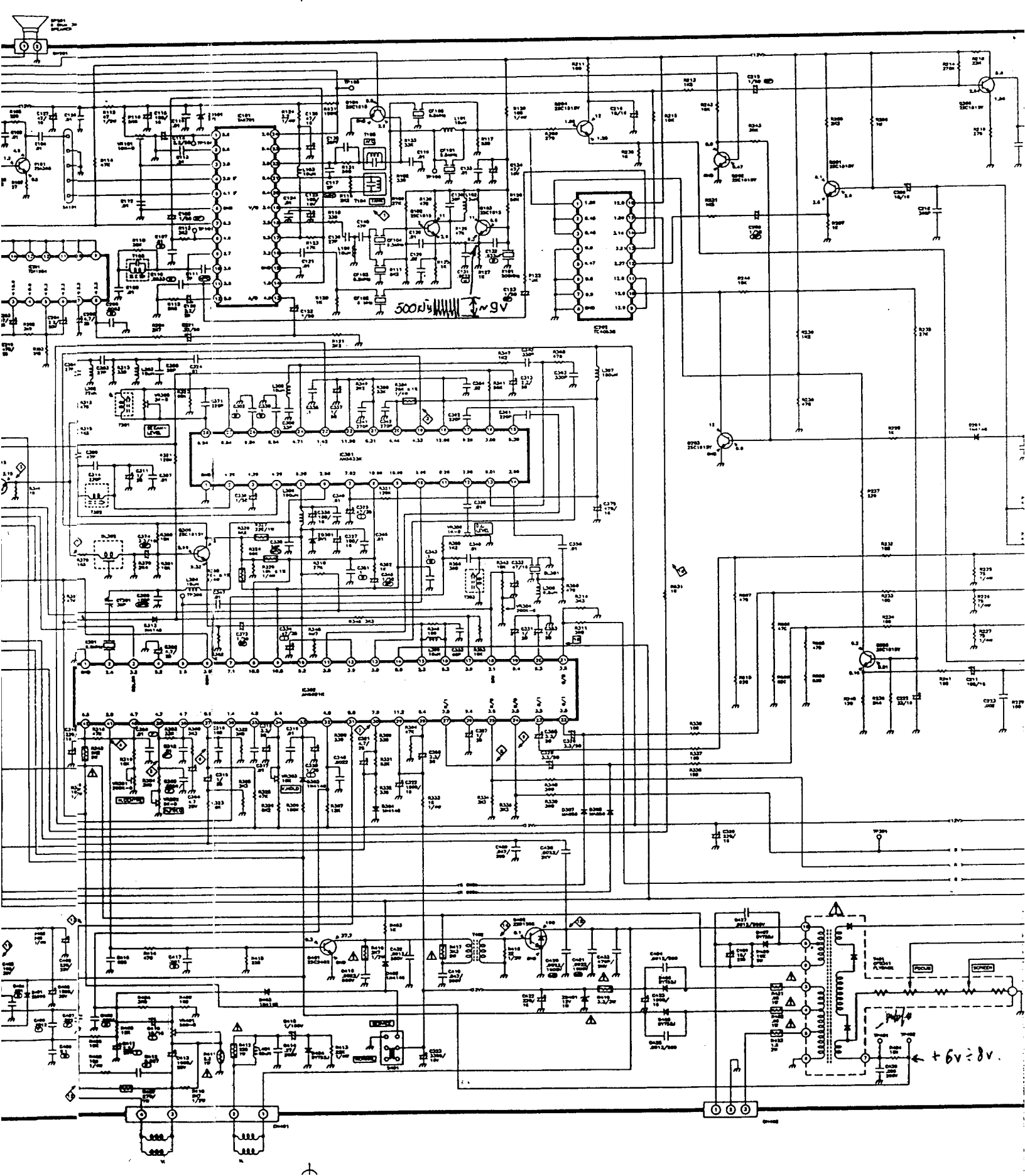
SCHEMATIC DIAGRAM FOR PAL SECAM B/G D/K SYSTEM

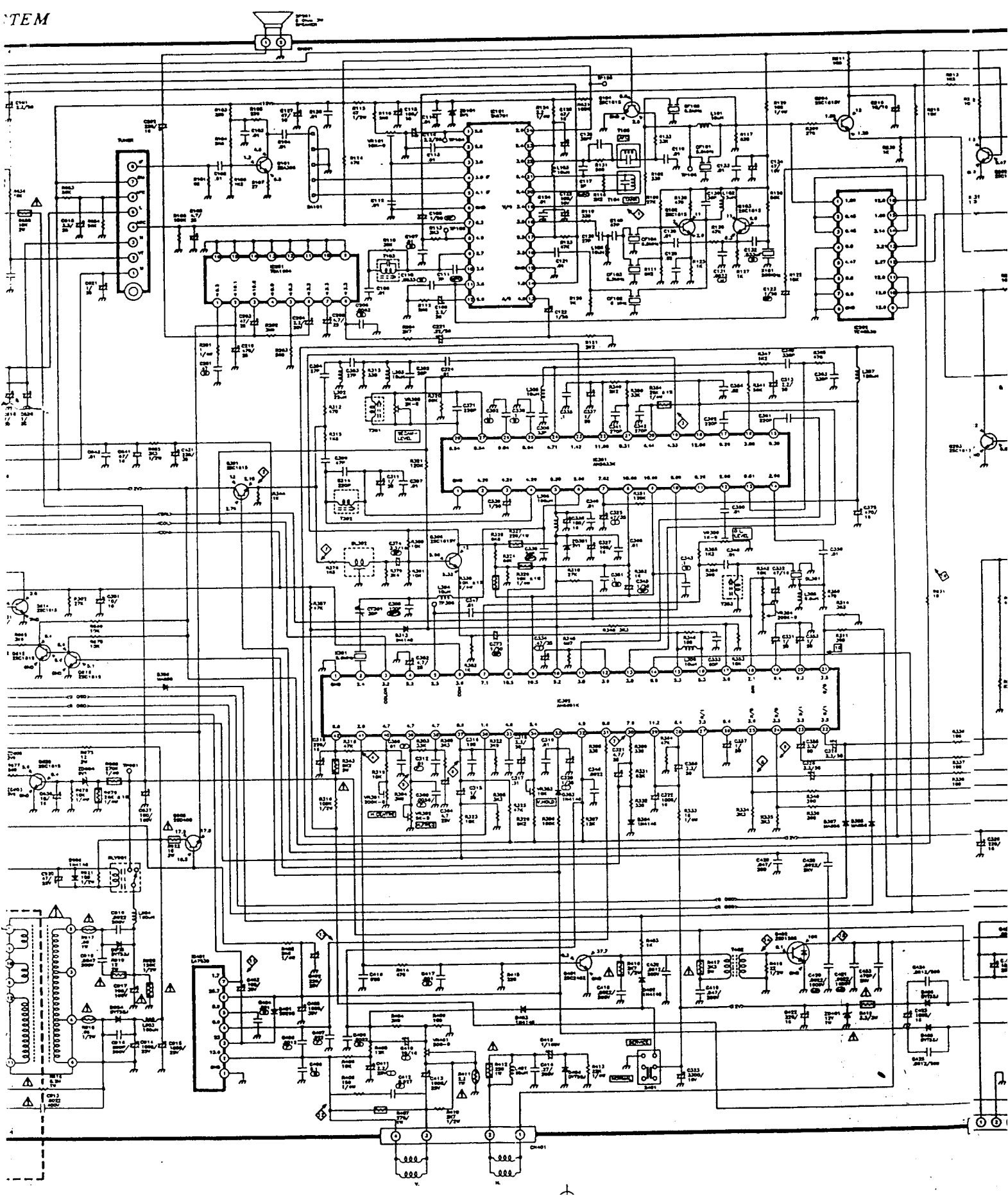
RECEIVE BOARD MAIN BOARD



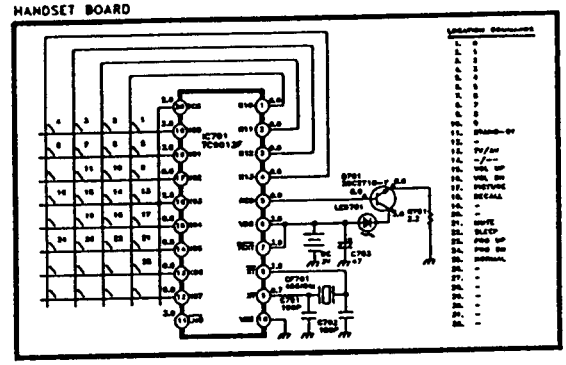
DRAWING NO. : 8321-07 (BF) (AKAI)

REV NO. : 0

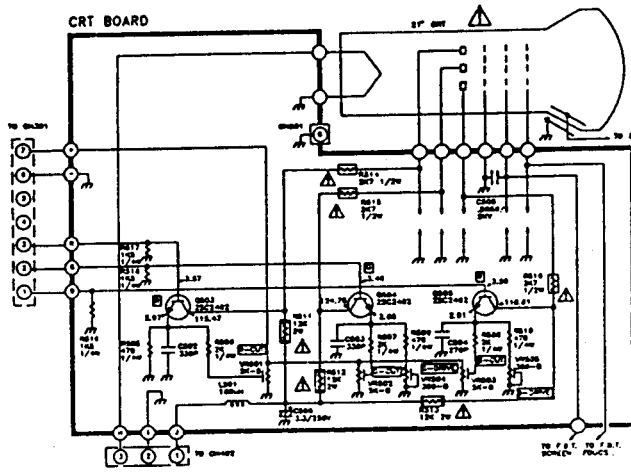

+6v ~ 8v.



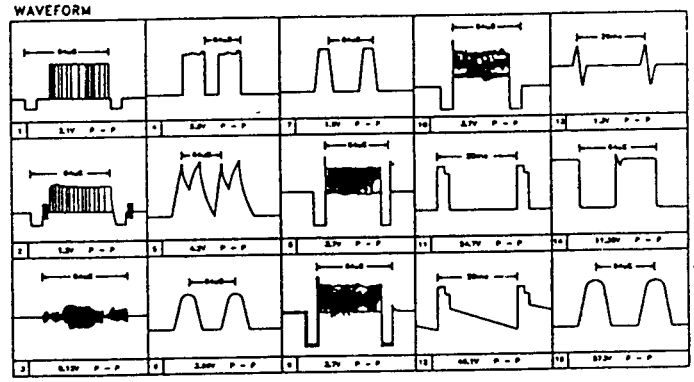
HANDSET BOARD



CRT BOARD



WAVEFORM



NOTE:

(1) ALL CAPACITORS ARE IN uF UNLESS OTHERWISE NOTED. ALL CAPACITORS ARE 50vw UNLESS OTHERWISE NOTED.
(2) CAPACITOR NOT SPECIFICALLY DESIGNATED ARE CERAMIC CAPACITORS.
(3) ALL RESISTORS ARE IN OHM 1/10 WATT UNLESS OTHERWISE NOTED.
(4) RESISTOR NOT SPECIFICALLY DESIGNATED ARE CARBON FILM RESISTORS.
(5) DC VOLTAGE ARE MEASURED FROM POINTS INDICATED TO THE CIRCUIT GROUND WITH A DIGITAL MULTIMETER TEST.
(6) WAVEFORMS ARE TAKEN WITH SETTING CONTROLS TO A NORMAL CONDITIONS (COLOR PHILIPS PATTERN).
(7) THIS CIRCUIT DIAGRAM IS SUBJECT TO CHANGE WITHOUT PRIOR NOTICE.
(8) ⚠ SAFETY CRITICAL DEVICE.

CIRCUIT SYMBOLS		
RESISTOR	CAPACITOR	
NONFLAMMABLE	ELECTROLYTIC	POLYPROPYLENE
FUSIBLE	ELECTROLYTIC	MYLAR
CEMENT	TANTALUM	
METAL OXIDE	METALLIZED POLYESTER	
THERMISTOR	POLYESTER FILM	

KONG WAH VIDEO ENGINEERING LTD.	
TITLE: SCHEMATIC DIAGRAM	
MODEL : K-8521DF	SYSTEM: PAL SECAM B/G D/K
DRAW NO. : 8521-07	PAGE : 07 OF 1
DRAWN BY:	APPROVED:
DATE: OCT 19, 98	REV NO. : 0